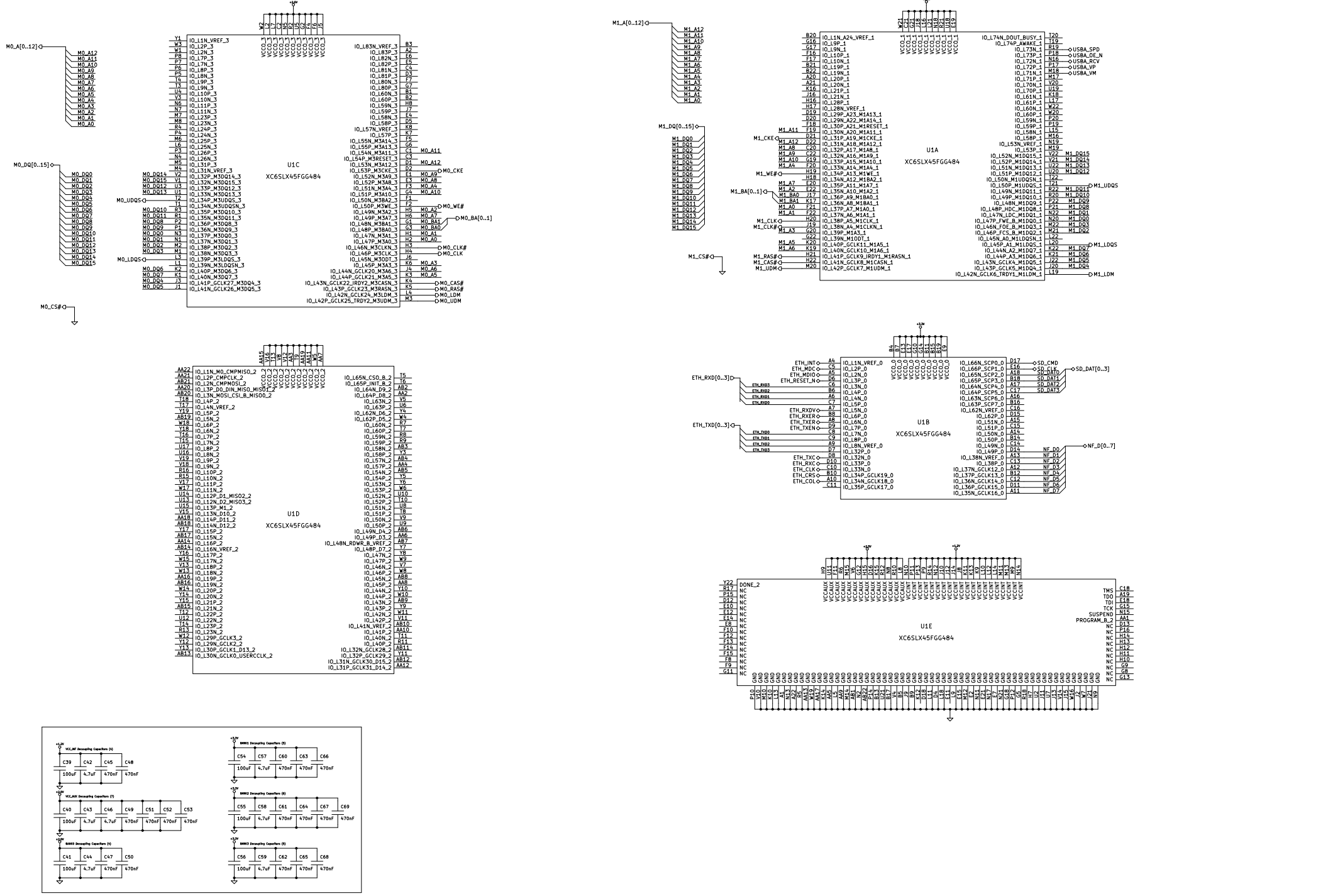
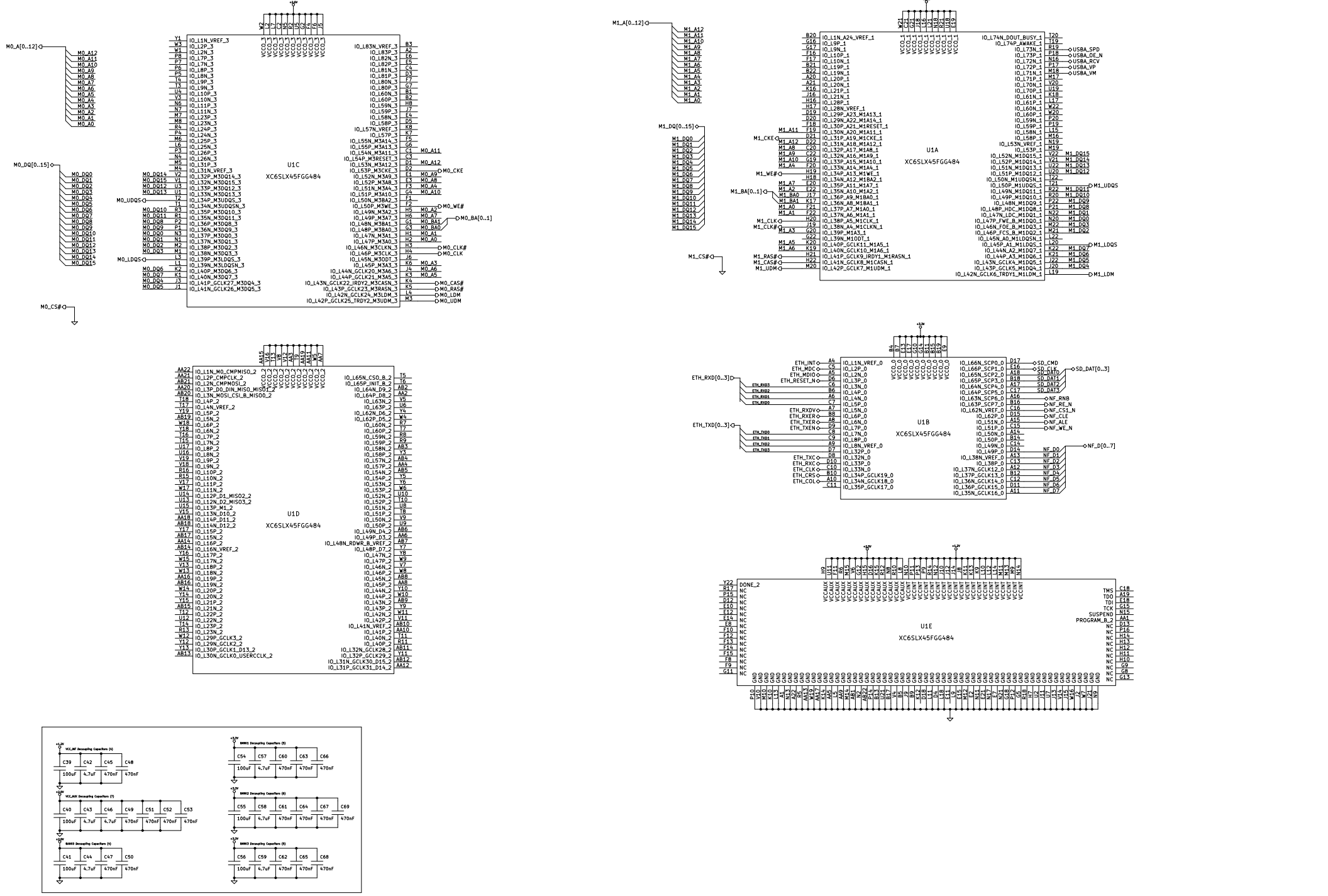

U1C
XC6SLX45FGG484
U1A
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1D
XC6SLX45FGG484
U1E
XC6SLX45FGG484
M0_A[0..12]
M0_DQ[0..15]
M0_CS#
M0_UDQS
M0_LDQS
M0_CKE
M0_WE#
M0_BA[0..1]
M0_CLK#
M0_CLK
M0_CAS#
M0_RAS#
M0_LDM
M0_UDM
M1_A[0..12]
M1_DQ[0..15]
M1_CS#
M1_CKE
M1_WE#
M1_BA[0..1]
M1_UDQS
M1_LDQS
M1_LDM
USBA_SPD
USBA_OE_N
USBA_RCV
USBA_VP
USBA_VM
SD_CMD
SD_CLK
SD_DAT0
SD_DAT1
SD_DAT2
SD_DAT3
SD_DAT[0..3]
ETH_RXD[0..3]
ETH_TXD[0..3]
NF_D[0..7]
DONE_2
TMS
TDO
TDI
TCK
SUSPEND
PROGRAM_B_2
VCC_INT Decoupling Capacitors (4)
VCC_AUX Decoupling Capacitors (7)
BANK0 Decoupling Capacitors (4)
BANK1 Decoupling Capacitors (5)
BANK2 Decoupling Capacitors (6)
BANK3 Decoupling Capacitors (5)


U1C
XC6SLX45FGG484
U1A
XC6SLX45FGG484
U1D
XC6SLX45FGG484
U1B
XC6SLX45FGG484
U1E
XC6SLX45FGG484
M0_A[0..12]
M0_DQ[0..15]
M0_CS#
M0_UDQS
M0_LDQS
M0_CKE
M0_WE#
M0_BA[0..1]
M0_CLK#
M0_CLK
M0_CAS#
M0_RAS#
M0_LDM
M0_UDM
M1_A[0..12]
M1_DQ[0..15]
M1_CS#
M1_UDQS
M1_LDQS
M1_LDM
USBA_SPD
USBA_OE_N
USBA_RCV
USBA_VP
USBA_VM
SD_CMD
SD_CLK
SD_DAT0
SD_DAT1
SD_DAT2
SD_DAT3
SD_DAT[0..3]
NF_RNB
NF_RE_N
NF_CS1_N
NF_CLE
NF_ALE
NF_WE_N
NF_D[0..7]
ETH_INT
ETH_MDC
ETH_MDIO
ETH_RESET_N
ETH_RXD[0..3]
ETH_TXD[0..3]
ETH_RXDV
ETH_RXER
ETH_TXER
ETH_TXEN
ETH_TXC
ETH_RXC
ETH_CLK
ETH_CRS
ETH_COL
DONE_2
TMS
TDO
TDI
TCK
SUSPEND
PROGRAM_B_2
VCC_INT Decoupling Capacitors (4)
VCC_AUX Decoupling Capacitors (7)
BANK0 Decoupling Capacitors (4)
BANK1 Decoupling Capacitors (5)
BANK2 Decoupling Capacitors (6)
BANK3 Decoupling Capacitors (5)
100uF
4.7uF
470nF